













Spectral Subtraction 06/06/2001 Input/Output Buffers $4 \times \text{oversampling} \Rightarrow$ ADC • we must process a 256-sample frame ¥ Input Buffer while ADC reads in 256 sample frame the next 64 samples. Since frames overlap, Output Buffer Г we must add results onto those from Overwrite existing DAC buffer contents previous frames. The last 64 samples of our result frame doesn't have any previous data to add on to, so we just overwrite the previous buffer contents. Use 1¼ frame circular buffers for input and output We have a 11/4 frame **algorithmic** delay (independent of processor speed) from input→output. 9